	Application No.	Application No. Applicant(s)	
Notice of Allowability	10/772,210	WU ET AL.	
	Examiner	Art Unit	
	Tu-Tu Ho	2818	
The MAILING DATE of this communication ap All claims being allowable, PROSECUTION ON THE MERITS herewith (or previously mailed), a Notice of Allowance (PTOL-INOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT of the Office or upon petition by the applicant. See 37 CFR 1.3	IS (OR REMAINS) CLOSED in 85) or other appropriate comm RIGHTS. This application is:	n this application. If not included unication will be mailed in due o	d ourse. THIS
1. X This communication is responsive to Paper filed 02/04/2	<u>2004</u> .		
2. The allowed claim(s) is/are 1-25.			
3. 🖾 The drawings filed on 04 February 2004 are accepted b	y the Examiner.		
 4. ☐ Acknowledgment is made of a claim for foreign priority a) ☐ All b) ☐ Some* c) ☐ None of the: 1. ☐ Certified copies of the priority documents head copies of the priority documents head copies of the certified copies of the priority International Bureau (PCT Rule 17.2(a)). * Certified copies not received: 	ave been received. ave been received in Applicatio	on No	on from the
Applicant has THREE MONTHS FROM THE "MAILING DAT noted below. Failure to timely comply will result in ABANDO THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	E" of this communication to file NMENT of this application.	e a reply complying with the requ	uirements
5. A SUBSTITUTE OATH OR DECLARATION must be sul INFORMAL PATENT APPLICATION (PTO-152) which (bmitted. Note the attached EX gives reason(s) why the oath o	AMINER'S AMENDMENT or NC r declaration is deficient.	TICE OF
 CORRECTED DRAWINGS (as "replacement sheets") n (a) including changes required by the Notice of Draftsp 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examin Paper No./Mail Date Identifying Indicia such as the application number (see 37 CFI each sheet, Replacement sheet(s) should be labeled as such in 	erson's Patent Drawing Review er's Amendment / Comment or R 1.84(c)) should be written on the	r in the Office action of	eack) of
 DEPOSIT OF and/or INFORMATION about the de attached Examiner's comment regarding REQUIREMEN 			ote the
Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-94) 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/S Paper No./Mail Date	8) 6. Interview S Paper No./ B/08), 7. Examiner's	formal Patent Application (PTO- ummary (PTO-413), /Mail Date Amendment/Comment Statement of Reasons for Allow	ŕ
of Biological Material	9. Other		
	A		
	avid Nelms ry Patent Examiner		
Technology Center 2800			

Art Unit: 2818

DETAILED ACTION

Allowable Subject Matter

1. Claims 1-25 are allowable over the prior art of record.

The following is an examiner's statement of reasons for allowance: The prior art of record fails to teach or render obvious a threshold voltage stabilizer for use with a MOS transistor having a body effect, a method of manufacturing thereof, and an integrated circuit comprising a threshold voltage stabilizer with all exclusive limitations as recited in claims 1, 10, and 18, comprising a body well in a substrate, a source in the body well, and a stabilization region below the body well, the threshold voltage stabilizer being configured to provide a stabilization voltage to the stabilization region to increase a depletion region within the body well thereby restricting the body effect to stabilize a threshold voltage of the MOS transistor.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

2. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Copies of the references are not being furnished with this Office Action per MPEP § 707.05(a).

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a) U.S. Patent 5,894,145 to Chen et al. discloses a dynamic random access memory

(DRAM) device having separate bias voltages for the memory area, the peripheral area, and the

I/O area of the DRAM device.

b) U.S. Patent 6,507,235 to Sher discloses an integrated circuit having a local charge

pumping circuit used to produce a first voltage in a localized region of a doped substrate and a

global charge pumping circuit used to produce a second voltage in all other portions of the

substrate outside of the localized region.

3. Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Tu-Tu Ho whose telephone number is (571) 272-1778. The

examiner can normally be reached on 6:30 am - 5:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, DAVID NELMS can be reached on (571) 272-1787. The fax phone number for the

organization where this application or proceeding is assigned is (703) 872-9306.

Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished

applications is available through Private PAIR only. For more information about the PAIR

system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR

system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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Tu-Tu Ho January 07, 2005 David Nelms
Supervisory Patent Examiner
Technology Center 2800

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